

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/653,149
Filing Date	
Inventor	. Garo J. Derderian, et al.
Assignee	. Micron Technology, Inc.
Group Art Unit	2818
Examiner	T. Lee
Attorney's Docket No	MI22-1330
Title: Capacitor Fabrication Methods Including Forming a C	onductive Layer

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To:

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)

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Spokane, WA 99201-3828

References - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated:

15 Aug 2003

Rv

James E. Lake

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AA 6,222,722			04/99	Fukuzumi et al						
	AΒ	6,204,070	3/01	Kim						
	A C	6,207,487	3/01	Kim et al						
	AD	6,391,803	5/02	Kim et al						
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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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LIST OF ART CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT Garo J. Derderian

GROUP 2818 FILING DATE August 31, 2000

U.S. PATENT DOCUMENTS

M. Comment					TENT DOCUMENTS				
*Examiner Initial		Document Number	Date	Date Name			Subclass	Filing Date If Appropriate	
	AA	09/590,795		Agarwal et 1/6/03)	al (as filed and amended 10/29/01 &			06:00	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPFP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.